

STN3906SF

PNP Silicon Transistor

Descriptions

- General small signal amplifier
- Switching application

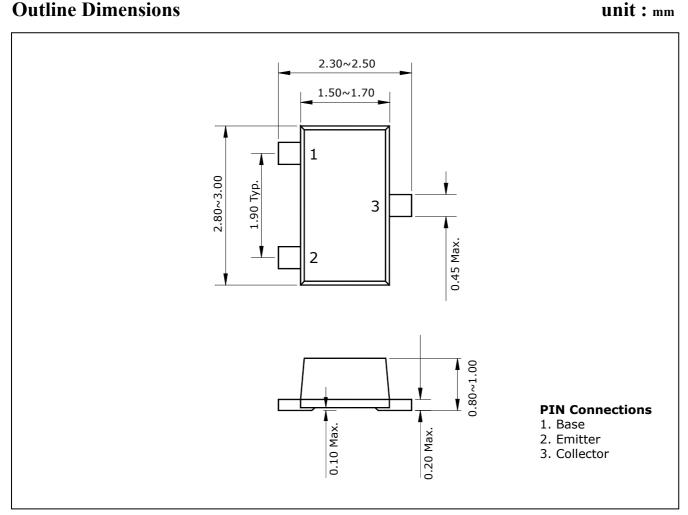
Features

- Collector saturation voltage : $V_{CE(sat)}$ =-0.5V(Max.) Low output capacitance : C_{ob} =4.5pF(Max.)
- Complementary pair with STN3904SF

Ordering Information

Type NO.	Marking	Package Code		
STN3906SF	JA	SOT-23F		

Outline Dimensions



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Absolute maximum ratings

(Ta=25°C)

Characteristic	Symbol	Ratings	Unit
Collector-Base voltage	V_{CBO}	-40	V
Collector-Emitter voltage	V_{CEO}	-40	V
Emitter-Base voltage	V_{EBO}	-5	V
Collector current	I_{C}	-100	mA
Collector dissipation	P _C *	350	mW
Junction temperature	T_{j}	150	°C
Storage temperature	T_{stg}	-55~150	°C

^{* :} Package mounted on 99.5% alumina 10*8*0.1mm

Electrical Characteristics

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Collector-Emitter breakdown voltage	BV _{CEO}	I _C =-1mA	-40	-	-	V
Collector-Base breakdown voltage	BV _{CBO}	I _C =-10μA	-40	-	ı	V
Emitter-Base breakdown voltage	BV _{EBO}	I _E =-10μA	-5	-	-	V
Collector cut-off current	I_{CBO}	V_{CB} =-40V, I_{E} = 0	-	-	-0.1	μА
DC current gain	h _{FE}	V_{CE} =-1V, I_{C} =-10mA	100	-	300	-
Collector-Emitter saturation voltage	$V_{\text{CE(sat)}}$	I_C =-50mA, I_B =-5mA	-	-	-0.5	V
Transition frequency	f_T	V_{CE} =-20V, I_{C} =-10mA	250	-	-	MHz
Collector output capacitance	C _{ob}	V_{CB} =-5V, I_E =0, f=1MHz	-	-	4.5	pF

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Electrical Characteristic Curves

Fig. 1 P_C - T_a

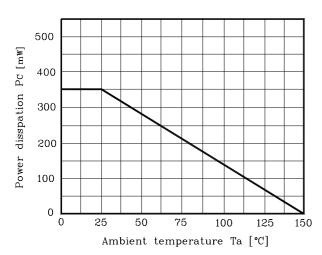


Fig. 3 h_{FE} - I_C

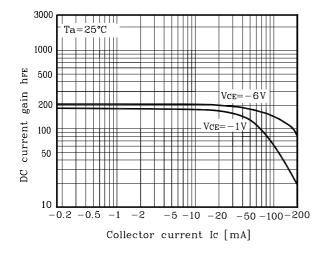


Fig. 5 $V_{CE(sat)}$ - I_{C}

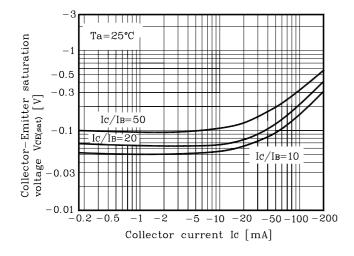


Fig. 2 I_C - V_{BE}

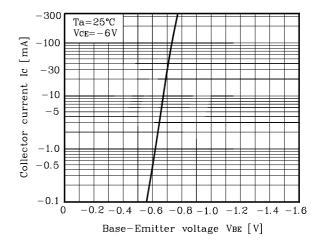
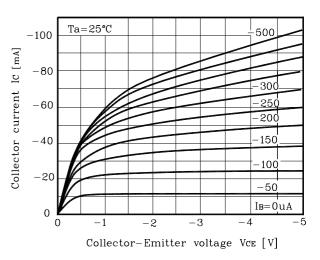


Fig. 4 I_{C} - V_{CE}



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